

**K. D. K. College of Engineering, Nagpur**  
**Department of Electronics Engineering**



**Attainment (2020-21)**

Academic Year: 2020-21

Internal Assessment Sheet

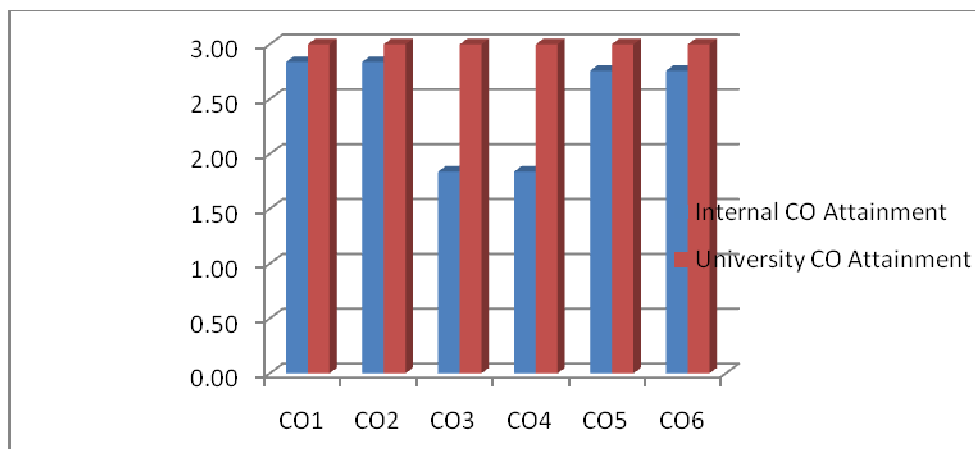
Program : B.E. Electronics Engineering

Semester: VIII Sem B.E. (E-III)

Course/Course Code: DATA COMPRESSION & ENCRYPTION (DCE)[BEENE805T]

C805.1	Implement the various text compression techniques.
C805.2	Implement various Audio compression techniques.
C805.3	Implement various Image and Video compression technique.
C805.4	To gain the knowledge of Encryption techniques.
C805.5	To know the application of the Encryption techniques to Digital communication.
C805.6	To provide various authentication using Digital communication.

CO Attainment						
	CO1	CO2	CO3	CO4	CO5	CO6
Internal CO Attainment	2.84	2.84	1.84	1.84	2.76	2.76
University CO Attainment	3.00	3.00	3.00	3.00	3.00	3.00
Actual CO Attainment = 80% UCOA + 20% ICOA	2.97	2.97	2.77	2.77	2.95	2.95
Consolidated CO attainment of the subject						2.90



Academic Year: 2020-21

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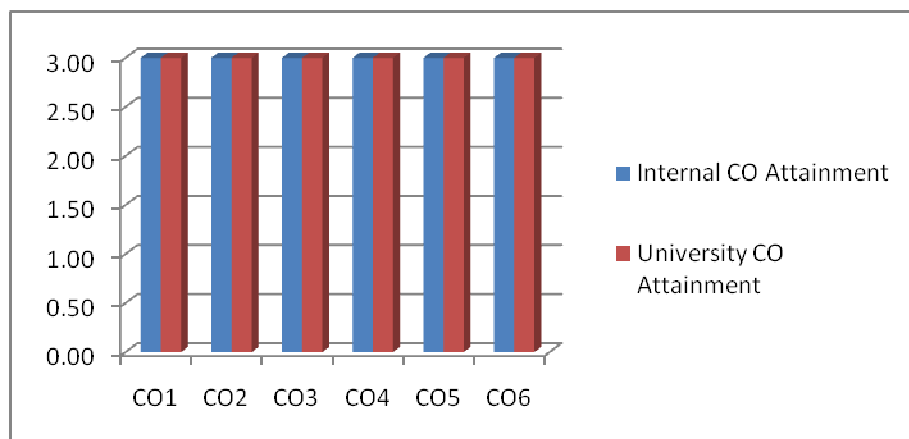
Program : B.E. Electronics Engineering

Semester: VII Sem B.E.

Course/Course Code: Advanced Digital System Design (ADSD)[BEENE704T]

C704.1	Design and model digital circuits with HDL at behavioral, structural, and RTL.
C704.2	Design and optimize complex Combinational and sequential circuits.
C704.3	Design digital systems that are reconfigurable for testing.
C704.4	Design examples on finite state machines.
C704.5	Simulate and synthesize programming models for digital circuits using ISE and Quartus tools.
C704.6	Experimentation on Hardware / software co-design ( FPGA design)

CO Attainment						
	CO1	CO2	CO3	CO4	CO5	CO6
Internal CO Attainment	3.00	3.00	3.00	3.00	3.00	3.00
University CO Attainment	3.00	3.00	3.00	3.00	3.00	3.00
<b>Actual CO Attainment = 80% UCOA + 20% ICOA</b>	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>
<b>Consoliated CO attainment of the subject</b>						<b>3.00</b>



Academic Year: 2020-21

Internal Assessment Sheet

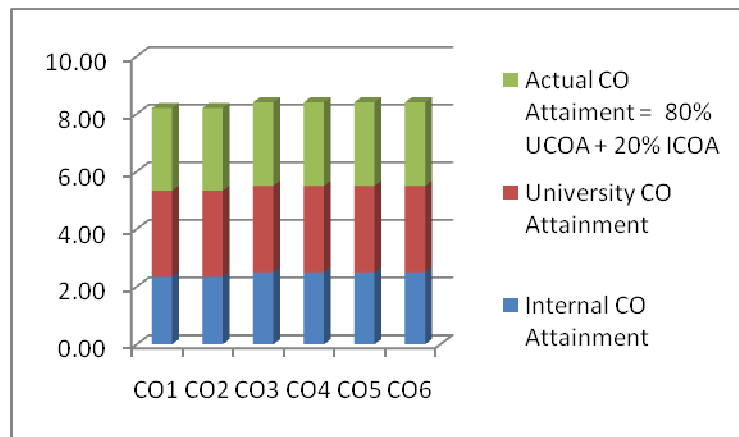
Program : B.E. Electronics Engineering

Semester: VI Sem B.E.

Course/Course Code: Digital Communication Networks (DCOM)[ BEENE604T]

C604.1	Describe the digital communication system & Analogy between vectors & Signal spaces.
C604.2	Explain receiver techniques for detection of a signal in AWGN channel.
C604.3	Demonstrate the concept of Source coding and decoding techniques.
C604.4	Describe digital modulation techniques.
C604.5	Demonstrate the concept of Waveform coding and decoding techniques.
C604.6	Describe spread spectrum analysis.

CO Attainment						
	CO1	CO2	CO3	CO4	CO5	CO6
Internal CO Attainment	2.33	2.33	2.50	2.50	2.50	2.50
University CO Attainment	3.00	3.00	3.00	3.00	3.00	3.00
<b>Actual CO Attainment = 80% UCOA + 20% ICOA</b>	<b>2.87</b>	<b>2.87</b>	<b>2.90</b>	<b>2.90</b>	<b>2.90</b>	<b>2.90</b>
<b>Consolidated CO attainment of the subject</b>						<b>2.89</b>



Academic Year: 2020-21

Internal Assessment Sheet

Program : B.E. Electronics Engineering

Semester: V Sem B.E.

Course/Course Code: IEED [BEENE505T]

C505.1 To understand the scope of an industrial economics and entrepreneurship development.

C505.2 To Understand key areas of business development

C505.3 To know sources of finance for business development.

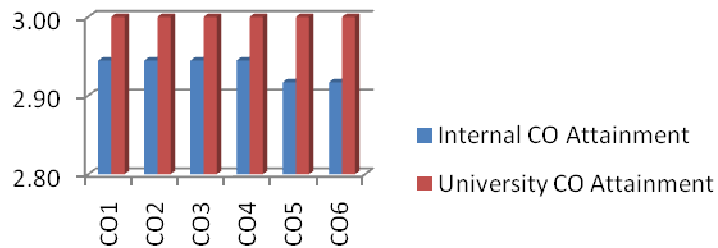
C505.4 To understand project preparation Techniques.

C505.5 To Understand various methods of taxation and tax benefits

C505.6 To understand significance of entrepreneurship and economic growth.

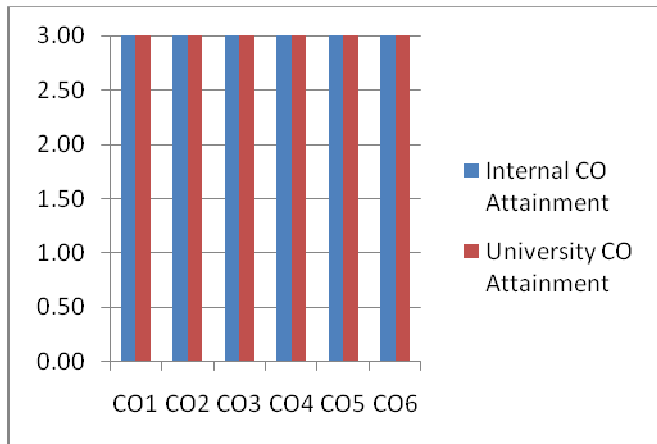
**CO Attainment**

	CO1	CO2	CO3	CO4	CO5	CO6
Internal CO Attainment	2.94	2.94	2.94	2.94	2.92	2.92
University CO Attainment	3.00	3.00	3.00	3.00	3.00	3.00
<b>Actual CO Attainment = 80% UCOA + 20% ICOA</b>	<b>2.99</b>	<b>2.99</b>	<b>2.99</b>	<b>2.99</b>	<b>2.98</b>	<b>2.98</b>
<b>Consolidated CO attainment of the subject</b>						<b>2.99</b>



Academic Year: 2020-21	
Internal Assessment Sheet	
Program : B.E. Electronics Engineering	
Semester: IV Sem B.E.	
Course/Course Code: Digital Circuits and Fundamental of Microprocessor [BEENE404T]	
C404.1	To understand the Combinational Circuits using Logic Gates.
C404.2	To design Arithmetic and Logical Circuits.
C404.3	To understand the Sequential Logic Circuits using Flip-Flop.
C404.4	To Design a Sequential Circuits such as Register, Counter and Sequence Generator.
C404.5	To understand the Digital Logic Family.
C404.6	To understand the Basic Fundamentals of 8085 Microprocessor.

CO Attainment						
	CO1	CO2	CO3	CO4	CO5	CO6
Internal CO Attainment	3.00	3.00	3.00	3.00	3.00	3.00
University CO Attainment	3.00	3.00	3.00	3.00	3.00	3.00
Actual CO Attainment = 80% UCOA + 20% ICOA	3.000	3.000	3.000	3.000	3.000	3.000
Consolidated CO attainment of the subject						3.0000



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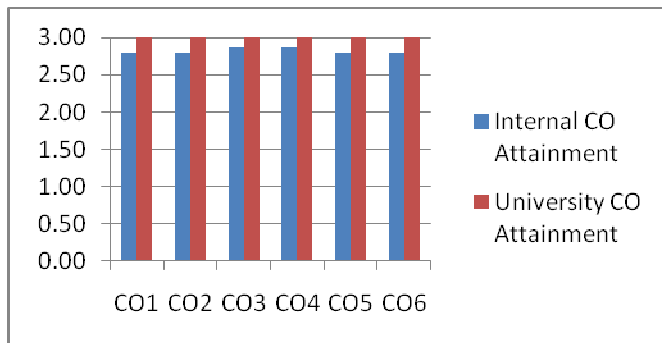
Program : B.E. Electronics Engineering

Semester: III Sem B.E.

Course/Course Code: Electronics Devices & Circuits (EDC)[BEENE302T]

C302.1	To explain the details of semi-conductor components Diode and its application.
C302.2	To understand the construction and biasing of transistors.
C302.3	To analyze transistor in AC and its use as a negative feedback amplifier.
C302.4	To use Transistor as a Oscillator and Multivibrator.
C302.5	To use Transistor as a various types of Power amplifier.
C302.6	To analyze and design amplifier circuits employing FET devices.

CO Attainment						
	CO1	CO2	CO3	CO4	CO5	CO6
Internal CO Attainment	2.80	2.80	2.86	2.86	2.79	2.79
University CO Attainment	3.00	3.00	3.00	3.00	3.00	3.00
Actual CO Attainment = 80% UCOA + 20% ICOA	2.961	2.961	2.972	2.972	2.958	2.958
Consolidated CO attainment of the subject						2.9635



# PO-PSO Attainment

Academic Year: 2020-21

Internal Assessment Sheet

Program : B.E. Electronics Engineering

Semester: VIII Sem B.E.

Course/Course Code: **Data Compression and Encryption (DCE) (BEENE805T)**

## PO Attainment

### CO-PO Mapping & extent of corelation

CO\PO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PS01	PS02
BEENE805T.1	3	3	2	1	-	-	-	1	-	-	1	2	3	3
BEENE805T.2	3	3	3	2	-	-	-	1	-	-	-	2	3	3
BEENE805T.3	3	3	3	2	2	1	-	1	-	-	-	2	3	3
BEENE805T.4	3	3	3	1	2	2	-	2	-	-	-	2	3	3
BEENE805T.5	3	3	3	1	2	2	1	2	-	-	-	2	3	3
BEENE805T.6	3	3	3	2	-	1	1	2	-	-	-	2	3	3
BEENE805T	3.00	3.00	2.83	1.50	2.00	1.50	1.00	1.50	-	-	1.00	2.00	3.00	3.00

Academic Year: 2020-21

Internal Assessment Sheet

Program : B.E. Electronics Engineering

Semester: VII Sem B.E.

Course/Course Code: **Advanced Digital System Design (ADSD) (BEENE704T)**

## PO Attainment

### CO-PO Mapping & extent of corelation

CO\PO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PS01	PS02
BEENE704T.1	3	2	2	-	1	-	-	-	1	-	-	2	3	1
BEENE704T.2	3	2	2	1	2	-	-	-	1	2	-	2	3	2
BEENE704T.3	3	2	2	1	2	-	-	-	1	2	-	2	3	2
BEENE704T.4	3	2	2	1	2	-	-	-	1	2	-	2	3	2
BEENE704T.5	3	2	2	2	2	-	-	-	1	2	-	2	3	2
BEENE704T.6	2	2	2	1	2	-	-	-	1	2	-	2	3	1
BEENE704T	2.83	2.00	2.00	1.20	1.83	-	-	-	1.00	2.00	-	2.00	3.00	1.67



Academic Year: 2020-21

Internal Assessment Sheet

Program : B.E. Electronics Engineering

Semester: VI Sem B.E.

Course/Course Code: **Digital Communication (DCom) (BEENE604T)**

PO Attainment

CO-PO Mapping & extent of corelation

CO\PO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PS01	PSO2
BEENE604T.1	3	2	2	-	1	-	-	-	-	-	-	2	3	1
BEENE604T.2	3	2	2	1	1	-	-	-	-	-	-	2	3	1
BEENE604T.3	3	2	2	1	1	-	-	-	-	-	-	2	3	1
BEENE604T.4	3	2	2	1	1	-	-	-	-	-	-	2	3	1
BEENE604T.5	3	2	2	2	1	-	-	-	-	-	-	2	3	1
BEENE604T.6	2	2	2	1	1	-	-	-	-	-	-	2	3	1
BEENE604T	2.83	2.00	2.00	1.20	1.00	-	-	-	-	-	-	2.00	3.00	1.00

Academic Year: 2020-21

Internal Assessment Sheet

Program : B.E. Electronics Engineering

Semester: V Sem B.E.

Course/Course Code: **IEED (BEENE505T)**

PO Attainment

CO-PO Mapping & extent of corelation

CO\PO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PS01	PSO2
BEENE505T.1	3	1	2	2	2	-	-	-	-	1	-	1	3	3
BEENE505T.2	3	2	1	1	2	-	-	-	-	1	-	1	3	2
BEENE505T.3	3	2	2	1	1	-	-	-	-	1	-	2	3	2
BEENE505T.4	2	2	2	1	2	-	-	-	-	1	-	2	3	2
BEENE505T.5	2	2	2	1	1	-	-	-	-	1	-	2	3	3
BEENE505T.6	2	2	2	1	1	-	-	-	-	1	-	2	3	2
BEENE505T	2.50	1.83	1.83	1.17	1.50	-	-	-	-	1.00	-	1.67	3.00	2.33

**Academic Year: 2020-21**

**Internal Assessment Sheet**

**Program : B.E. Electronics Engineering**

**Semester: IV Sem B.E.**

**Course/Course Code: DCFM (BEENE404T)**

**PO Attainment**

CO-PO Mapping & extent of correlation

CO/PO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PS01	PSO2
BEETRX404T.1	3	3	2	-	1	1	-	-	-	-	-	-	3	1
BEETRX404T.2	3	3	3	2	2	1	-	-	-	-	-	-	3	2
BEETRX404T.3	3	3	3	-	2	2	-	-	-	-	-	-	3	2
BEETRX404T.4	3	3	2	-	2	2	-	-	-	-	-	-	3	2
BEETRX404T.5	3	3	3	-	2	1	-	-	-	-	-	-	3	2
BEETRX404T.6	3	3	2	-	2	2	-	-	-	-	-	-	3	2
<b>BEETRX404T</b>	<b>3</b>	<b>3</b>	<b>2.5</b>	<b>2</b>	<b>1.83</b>	<b>1.5</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>3</b>	<b>1.83</b>

**Academic Year: 2020-21**

**Internal Assessment Sheet**

**Program : B.E. Electronics Engineering**

**Semester: III Sem B.E.**

**Course/Course Code: EDC (BEENE302T)**

**PO Attainment**

CO-PO Mapping & extent of correlation

CO/PO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PS01	PSO2
BEENE302T.1	3	3	2	1	-	-	-	-	-	-	-	2	3	3
BEENE302T.2	3	3	3	2	-	-	-	-	-	-	-	2	3	2
BEENE302T.3	3	3	3	1	-	-	-	-	-	-	-	2	3	2
BEENE302T.4	3	3	2	1	-	-	-	-	-	-	-	2	3	3
BEENE302T.5	3	3	2	1	-	-	-	-	-	-	-	2	3	2
BEENE302T.6	3	3	3	1	-	-	-	-	-	-	-	2	3	2
<b>BEENE302T</b>	<b>3.00</b>	<b>3.00</b>	<b>2.50</b>	<b>1.17</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>2.00</b>	<b>3.00</b>	<b>2.33</b>